

Compal Confidential

CIUSA/SB/SD

Cruze I/O Board Schematic Document

LS-E542P

2017-01-06

REV:1.0

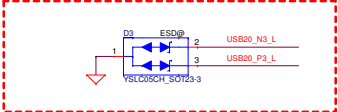
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USB2.0 Port

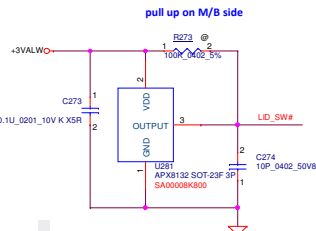
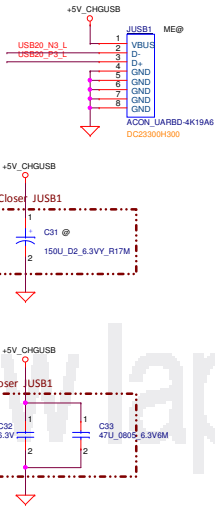
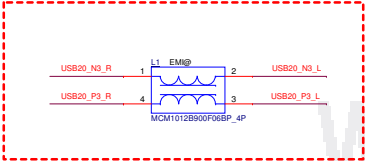
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I/O CONN

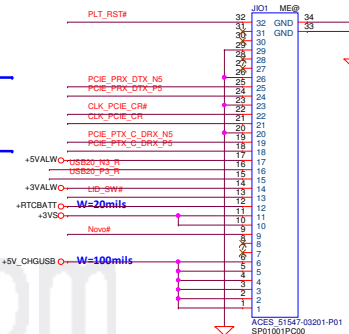
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EMI



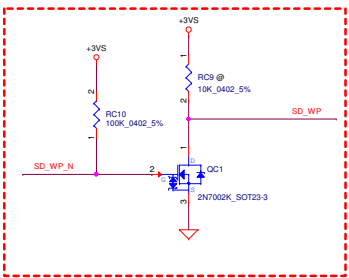
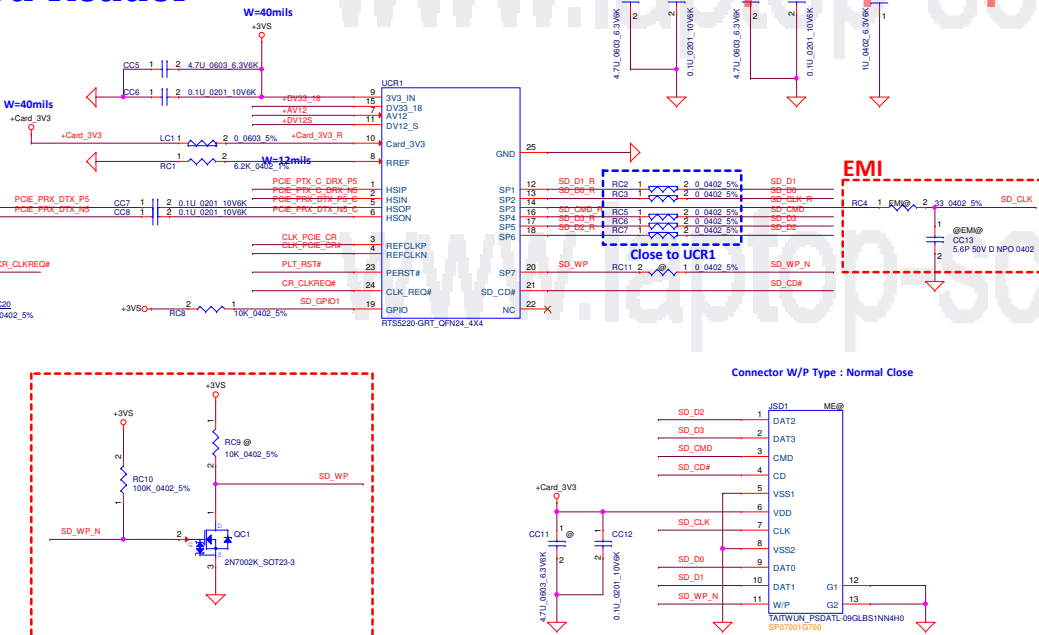
Card Reader



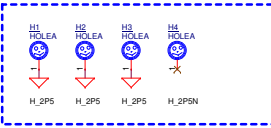
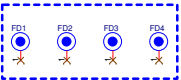
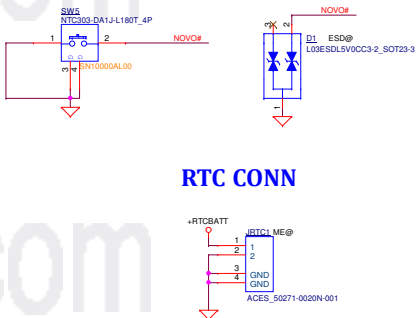
Card Reader

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Novo Button



RTC CONN



For W/P normal close type connector

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				Date:	Monday, January 09, 2017
				Sheet	2 of 3

Item	Reason for change	PG#	Modify List	Date	Phase
1	ME modify	2	Remove H5	09/15	EVT to DVT
2	DFX Request (Factory Rule)	2	Remove R101 and R102 (Remove 0-ohm path, remain choke)	11/09	DVT to PVT
3					
4					
5					
6					
7					
8					
9					
10					
11					
12					
13					
14					
15					

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				HW PIR	
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